

**UNIT I****Chapter 1 : Introduction and Architecture of 8051****1-1 to 1-23**

1.1	Introduction to Microprocessors .....	1-1	1.4.1(M)	Program Counter (PC).....	1-12
1.1.1	Some Basic Terms used in Microprocessors .....	1-1	1.5	Internal RAM Organization and Register Structure.....	1-12
1.1.2	Microprocessor Characteristics and Functions....	1-1	1.5.1	Special Function Registers (SFRs).....	1-12
1.1.2(A)	Characteristics of Microprocessor.....	1-1	1.5.2	Four Register Banks of 8 Bytes .....	1-14
1.1.2(B)	Functions of a Microprocessor.....	1-2	1.5.3	External Memory Interface.....	1-14
1.1.3	Block Diagram of a Microcomputer.....	1-2	1.6	Programmers Model of 8051.....	1-14
1.1.4	Buses and Memory Accessing.....	1-3	1.7	Pin Functions of 8051.....	1-15
1.1.4(A)	Address Bus.....	1-3	1.8	Ports Structure of 8051 .....	1-17
1.1.4(B)	Data Bus.....	1-4	1.8.1	Port 0 .....	1-18
1.1.4(C)	Control Bus.....	1-4	1.8.1(A)	Reading from a Port Pin .....	1-18
1.1.5	ALU (Arithmetic and Logic Unit).....	1-4	1.8.1(B)	Writing to a Port.....	1-18
1.1.6	Basic Important Terms.....	1-5	1.8.1(C)	Port Loading and Interfacing.....	1-18
1.2	Introduction to Microcontroller.....	1-5	1.8.1(D)	Read-Modify-Write Feature.....	1-18
1.2.1	Microprocessor .....	1-5	1.8.2	Port 1 .....	1-18
1.2.2	Microcontroller .....	1-5	1.8.2(A)	Reading from a Port Pin .....	1-18
1.2.3	Comparison of Microprocessors and Microcontrollers.....	1-6	1.8.2(B)	Writing to a Port.....	1-18
1.2.4	Advantages of Microcontrollers over Microprocessors.....	1-7	1.8.2(C)	Port Loading and Interfacing.....	1-19
1.2.5	Disadvantages/Limitations of 8 bit Microcontrollers over Microprocessors .....	1-7	1.8.2(D)	Read-Modify-Write Feature.....	1-19
1.2.6	Applications of Microcontrollers.....	1-7	1.8.3	Port 2 .....	1-19
1.3	Introduction to 8051.....	1-7	1.8.3(A)	Reading from a Port Pin .....	1-19
1.3.1	Features of 8051.....	1-7	1.8.3(B)	Writing to a Port.....	1-19
1.3.2	Other Members of 8051 Family .....	1-8	1.8.3(C)	Port Loading and Interfacing.....	1-19
1.4	Architecture of 8051 .....	1-8	1.8.3(D)	Read-Modify-Write Feature.....	1-19
1.4.1(A)	Accumulator (ACC).....	1-8	1.8.4	Port 3 .....	1-19
1.4.1(B)	B Register.....	1-8	1.8.4(A)	Reading from a Port Pin .....	1-20
1.4.1(C)	Arithmetic and Logic Unit (ALU).....	1-9	1.8.4(B)	Writing to a Port.....	1-20
1.4.1(D)	Program Status Word.....	1-9	1.8.4(C)	Port Loading and Interfacing.....	1-20
1.4.1(E)	Clock and Oscillator .....	1-10	1.8.4(D)	Read-Modify-Write Feature.....	1-20
1.4.1(F)	Stack Pointer .....	1-11	1.9	Timers and Counters of 8051 .....	1-20
1.4.1(G)	Stack and Bank 1 Conflict.....	1-11	1.10	Serial Interface of 8051 .....	1-21
1.4.1(H)	Data Pointer.....	1-11	1.11	Power Saving Modes .....	1-21
1.4.1(I)	Ports 0 to 3.....	1-12	1.11.1	The Power Mode Control (PCON) Special Function Register .....	1-21
1.4.1(J)	Timers and Counters.....	1-12	1.11.2	Power Saving Modes of Operation .....	1-21
1.4.1(K)	Serial Data Buffer .....	1-12	1.11.2(A)	Power Down Operation.....	1-21
1.4.1(L)	on-Chip Oscillator .....	1-12	1.11.2(B)	Idle Mode Operation.....	1-22
			1.12	8051 Interrupts and their Priority.....	1-22
			1.13	Exam Pack (Review and University Questions).....	1-22

**UNIT I & II****Chapter 2 : Instruction Set 2-1 to 2-36**

2.1	Addressing Modes .....	2-1
2.1.1	Immediate Addressing .....	2-1
2.1.2	Direct Addressing .....	2-1
2.1.3	Indirect Addressing.....	2-1
2.1.4	Register Addressing.....	2-2
2.1.5	External Indirect.....	2-2
2.2	Instructions of 8051 .....	2-2
2.2.1	MOV <dest-byte>, <src-byte>.....	2-2
2.2.2	MOV DPTR, #data 16.....	2-4
2.2.3	MOVC A, @A+ <base register>.....	2-4
2.2.4	MOVX <dest-byte>, <src-byte>.....	2-5
2.2.5	Introduction to Stack.....	2-6
2.2.6	PUSH <direct>.....	2-6
2.2.7	POP<direct>.....	2-7
2.2.8	XCH A, <byte variable>.....	2-7
2.2.9	XCHD A, @Ri.....	2-8
2.3	Arithmetic Instructions.....	2-8
2.3.1	ADD A, <src-byte>.....	2-8
2.3.2	ADDC A, <src-byte>.....	2-9
2.3.3	SUBB A, <src-byte>.....	2-10
2.3.4	INC <byte>.....	2-11
2.3.5	INC DPTR.....	2-11
2.3.6	DEC <byte>.....	2-11
2.3.7	MUL AB.....	2-12
2.3.8	DIV AB.....	2-12
2.3.9	DAA .....	2-13
2.4	Byte Level Logical Operations .....	2-13
2.4.1	ANL <dest-byte>, <src-byte>.....	2-13
2.4.2	ORL <dest-byte>, <src-byte>.....	2-15
2.4.3	XRL <dest-byte>, <src-byte> .....	2-16
2.4.4	CLR A.....	2-17
2.4.5	CPL A.....	2-17
2.4.6	RL A.....	2-18
2.4.7	RLC A.....	2-18
2.4.8	RR A.....	2-18
2.4.9	RRC A.....	2-19
2.4.10	SWAP A.....	2-19
2.5	Bit Level Logical Operations.....	2-19

2.5.1	CLR Bit.....	2-19
2.5.2	SETB .....	2-20
2.5.3	CPL Bit.....	2-20
2.5.4	ANL C, <src-bit>.....	2-20
2.5.5	ORL C, <src-bit> .....	2-20
2.5.6	MOV <dest-bit>, <src-bit> .....	2-21
2.6	Program and Machine Control Instructions .....	2-21
2.6.1	ACALL addr11 .....	2-21
2.6.2	LCALL addr16.....	2-22
2.6.3	RET.....	2-23
2.6.4	RETI.....	2-23
2.6.5	AJMP addr11 .....	2-23
2.6.6	LJMP addr16.....	2-24
2.6.7	SJMP rel.....	2-24
2.6.8	JMP @A+DPTR .....	2-24
2.6.9	JZ rel .....	2-25
2.6.10	JNZ rel.....	2-25
2.6.11	JC rel .....	2-25
2.6.12	JNC rel.....	2-25
2.6.13	JB bit, rel .....	2-26
2.6.14	JNB bit, rel.....	2-26
2.6.15	JBC bit, rel .....	2-27
2.6.16	CJNE <dest-byte>, <src-byte>, rel.....	2-27
2.6.17	DJNZ <byte>, <rel-addr> .....	2-28
2.6.18	NOP .....	2-29
2.7	Instruction Comparisons.....	2-29
2.7.1	RET and RETI.....	2-29
2.7.2	AJMP, SJMP and LJMP.....	2-30
2.8	Summary of 8051 Instructions .....	2-31
2.8.1	Arithmetic Operators .....	2-31
2.8.2	Byte Level Logical Operations .....	2-31
2.8.3	Data Transfer .....	2-32
2.8.4	Bit Level Logical Operations.....	2-33
2.9	Assembler Directives .....	2-33
2.10	Exam Pack (Review and University Questions).....	2-35

**UNIT II****Chapter 3 : Assembly Language Programming****3-1 to 3-32**

3.1	Programs on Basic Operations.....	3-1
3.2	Exam Pack (Review and University Questions).....	3-31
➤	<b>Model Question Paper (In Sem.) .....</b>	<b>Q. 1 to Q. 2</b>

**UNIT III****Chapter 4 : 8051 Programming in C 4-1 to 4-8**

4.1	Programming in C.....	4-1
4.1.1	Bit Addressable I/O Programming.....	4-1
4.2	Data Types using in C Programming for 8051 ....	4-1
4.3	Checksum Byte .....	4-5
4.4	Data Serialization.....	4-5
4.5	8051 Hardware Connection.....	4-7
4.5.1	Intel Hex File.....	4-8
4.6	Exam Pack (Review and University Questions)...	4-8

**Chapter 5 : Timer Counter and Interrupt Programming  
5-1 to 5-25**

5.1	Timers and Counters of 8051 .....	5-1
5.1.1	Timer Mode Control (TMOD) Special Function Register .....	5-1
5.1.2	Timer Control (TCON) Special Function Register .....	5-1
5.1.3	Timer Mode 0 .....	5-2
5.1.4	Timer Mode 1 .....	5-2
5.1.5	Timer Mode 2 .....	5-2
5.1.6	Timer Mode 3 .....	5-2
5.2	Interrupts Structure of 8051 .....	5-3
5.2.1	8051 Interrupts and their Priority .....	5-4
5.2.2	Vector Addresses of 8051 Interrupts.....	5-4
5.2.3	Enabling and Disabling of Interrupts.....	5-4
5.2.4	Interrupt Priority .....	5-4
5.2.5	Timer Flag Interrupt.....	5-5
5.2.6	Serial Port Interrupt .....	5-5
5.2.7	External Interrupts.....	5-5
5.2.8	Reset is a Non-Maskable Interrupts.....	5-5
5.3	Programming the 8051 Timer / Counter.....	5-6
5.3.1	Programming the Timer in Mode 1.....	5-6
5.3.2	Mode 0 Programming .....	5-7
5.3.3	Programming the Timer in Mode 2.....	5-7
5.4	Counter and Pulse Width Measurement (PWM).....	5-9
5.5	Interrupts Vs. Polling.....	5-14
5.6	Design Problems.....	5-16
5.7	Exam Pack (Review and University Questions).....	5-25

**UNIT V****Chapter 6 : Serial Port Programming 6-1 to 6-29**

6.1	Serial Interface of 8051 .....	6-1
6.1.1	Serial Port Modes.....	6-1
6.1.1(A)	Mode 0 : Shift Register Mode .....	6-2
6.1.1(B)	Mode 1 : Standard UART .....	6-2
6.1.1(C)	Mode 2 : Multiprocessor mode.....	6-3
6.1.1(D)	Mode 3 : Multiprocessor Mode.....	6-3
6.1.2	Baud Rates.....	6-3
6.1.2(A)	Mode 0.....	6-3
6.1.2(B)	Modes 1 and 3 .....	6-4
6.1.2(C)	Mode 2.....	6-4
6.1.3	The Serial port Control (SCON) Special Function Register.....	6-4
6.2	Parallel Vs. Serial Communication.....	6-5
6.3	Types of Communication Systems.....	6-5
6.4	Serial Transmission Formats .....	6-6
6.4.1	Asynchronous Data Transfer .....	6-6
6.4.2	Synchronous Data Transfer .....	6-7
6.4.3	Comparison of Asynchronous and Synchronous Format.....	6-7
6.4.4	Baud Rate.....	6-8
6.5	RS 232 Standard.....	6-8
6.5.1	Signals used in RS232.....	6-8
6.6	8051 Connection to RS232 .....	6-10
6.7	Serial Communication Programming.....	6-11
6.7.1	Handling Serial Interrupt.....	6-13
6.7.2	Programming the 8051 to Transfer Data Serially.....	6-13
6.7.3	Programming the 8051 to Receive Data Serially.....	6-14
6.8	Design Programs.....	6-18
6.9	GSM Interfaces.....	6-26
6.9.1	GSM Modem and its Interfacing with Microcontroller .....	6-26
6.9.2	Interfacing GSM with 8051 .....	6-26
6.10	Exam Pack (Review and University Questions).....	6-29

**UNIT VI****Chapter 7 : Interfacing Keyboard and Display****7-1 to 7-21**

7.1	Keyboard .....	7-1
7.1.1	Key Switch Mechanism.....	7-1
7.1.2	Hardware Key Debouncing.....	7-1
7.1.3	Software Key Debouncing.....	7-1
7.2	Keyboard Interface Circuit.....	7-2
7.2.1	Non-matrix Type Keyboard .....	7-2
7.2.2	Matrix Keyboard Interface .....	7-2
7.3	Roll-Over Techniques.....	7-4
7.4	Display.....	7-7
7.4.1	LED Displays.....	7-7
7.4.2	Seven Segment Display (SSD) .....	7-11
7.4.3	Interfacing Liquid Crystal Display (LCD) to 8051 .....	7-12
7.4.4	Initialization of LCD .....	7-14
7.5	Exam Pack (Review and University Questions).....	7-21

**UNIT IV & VI****Chapter 8 : Measurement of Electrical Parameters****8-1 to 8-67**

8.1	Interfacing DAC and ADC .....	8-1
8.1.1	Features of DAC 0808 .....	8-1
8.1.2	Pin Configuration and Functional Block Diagram .....	8-1
8.2	Interfacing DAC, ADC and Sensors.....	8-8
8.3	A/D Converters .....	8-9

8.4	Interfacing ADC and DAC to MCS-51 Family .....	8-11
8.5	Measurement of Parameters like Pressure, Flow, Temperature, etc. ....	8-12
8.5.1	Flow Sensor .....	8-13
8.5.2	Pressure Sensor.....	8-13
8.5.3	Temperature Sensor LM35 .....	8-18
8.5.4	Frequency Measurement using 8051 .....	8-19
8.5.5	Measurement of Voltage and Current.....	8-23
8.5.6	Measurement of Power (KW) .....	8-26
8.5.7	KVA (Apparent Power) Measurement.....	8-26
8.5.8	Measurement of Power Factor .....	8-27
8.6	Interfacing Relays .....	8-39
8.7	Solid State Relay .....	8-39
8.8	Opto-isolator .....	8-40
8.9	Stepper Motor.....	8-40
8.9.1	4 Step Sequence and 8 Step Sequence.....	8-41
8.9.2	Step Angle.....	8-41
8.9.3	8051 Interfacing to Stepper Motor .....	8-41
8.9.4	Using Transistors as Drivers.....	8-42
8.9.5	Controlling Stepper Motor via Optoisolator .....	8-43
8.9.6	Wave Drive 4 Step Sequence.....	8-45
8.10	Interfacing of DC Motor .....	8-51
8.11	Interfacing DC Motor using L293 H-Bridge.....	8-52
8.12	Design Problems.....	8-55
8.13	Exam Pack (Review and University Questions).....	8-66

➤	<b>Lab Manual .....</b>	<b>L-1 to L-2</b>
➤	<b>Model Question Paper (End Sem.) .....</b>	<b>Q. 1 to Q. 2</b>

